





## Development History

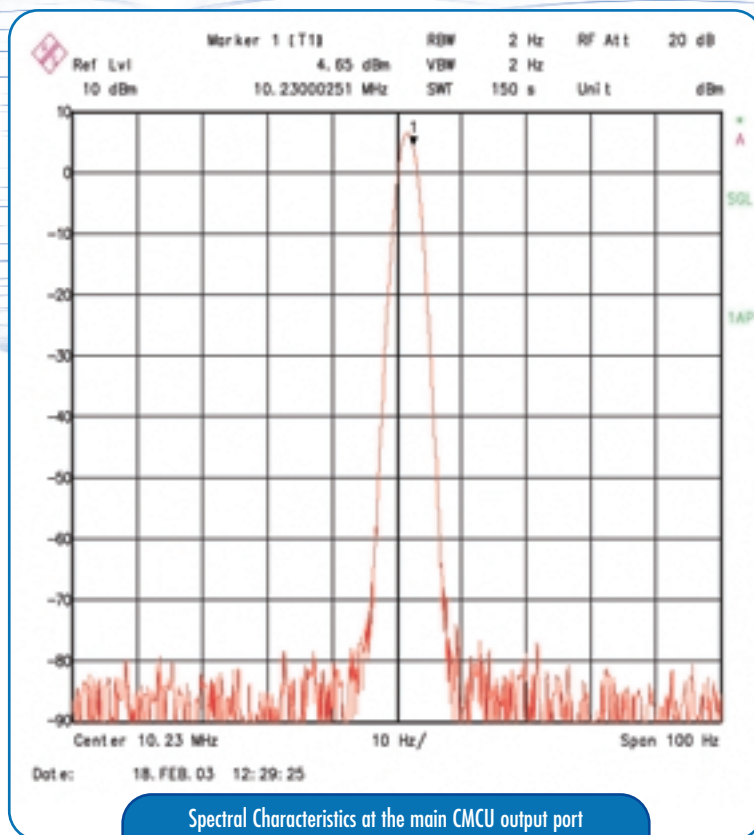
Programme kicked off in **Nov. 2001**

System Design Review (SDR) Completed in **Oct. 2002**

Electrical Design Finished

Elegant Breadboard (EBB) manufactured and tested

Electrical Tests are in progress



Spectral Characteristics at the main CMCU output port



### Coming soon:

- Engineering Model (EM) manufacturing and test
- Equipment to be provided for GSTB V2

#### Industry Contact:

Alcatel Espacio/TNT  
Mr. Jose Maria Ruiz  
Tel: +39 918077915  
Email: [Jose\\_Maria.Ruiz\\_Minguez@alcatel.es](mailto:Jose_Maria.Ruiz_Minguez@alcatel.es)

#### ESA Contact:

Francesco Emma  
Tel. + 31 71 565-5002  
Email: [francesco.emma@esa.int](mailto:francesco.emma@esa.int)

GalileoTech News are being released on a case by case basis and are intended for general information only. For more comprehensive and up-to-date information please contact the Galileo Project Office at ESA-ESTEC

Tel +31 71 565 3193

[galileo.project@esa.int](mailto:galileo.project@esa.int)

[www.esa.int/navigation](http://www.esa.int/navigation)

