The Clock Monitoring and Control Unit (CMCU) is an integral part of the Galileo navigation Payload which interfaces the onboard Atomic Frequency Standards and performs the following tasks:

- Generates the reference frequency at 10.23 MHz
- Implements a “phase comparison” system for the control of the redundant RAFS clock with the main S-PHM clock
- Interfaces with the on-board Data Handling System

Main Features:

- Volume: 4.0 litres
- Output power (Main port): 3.0 dBm
- No. of output ports: 4
- Output frequency: 10.23 MHz
- Close to carrier spurious: < -84 dBc
Development History

Programme kicked off in Nov. 2001


Electrical Design Finished

Elegant Breadboard (EBB) manufactured and tested

Electrical Tests are in progress

Coming soon:

- Engineering Model (EM) manufacturing and test
- Equipment to be provided for GSTB V2

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